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# LatticeXP2 Standard Evaluation Board

The LatticeXP2 Standard Evaluation Board is a flexible platform designed to help you quickly evaluate the features and performance of the LatticeXP2 FPGA, or aid in development of custom designs. For more PCB options, see the **LatticeXP2 Advanced Evaluation Board**.

**click for enlarged view**

## Product Contents

Each LatticeXP2 Standard Evaluation Board includes the following:

LatticeXP2 Standard Evaluation Board featuring

- LatticeXP2-17 FPGA in 484fpBGA package (LFXP2-17E-6F484C)
- On-board Asynch SRAM memory (256Kx32 providing 1Mbyte)
- A/D converter (Burr Brown ADS7842)
- D/A converter (Burr Brown DAC7617)
- 10K digital POT
- RS232 DB9 "female" connector
- Compact Flash connector
- 8-bit switch
- 8 general purpose LEDs
- 4 push-button switches
- 7-segment LED
- Built-in USB download capability (includes MachXO device)
- Selectable I/O voltage
- SMA connectors for clock and general purpose I/O
- PAC607 power manager
- on-board oscillator (dip socket)
- Proto/test area
- SPI flash memory for alternate configuration
- Power via Bellnix DC power control modules
- LCD connector

USB Cable for download (Note: this board has a Lattice download cable built-in, so only a standard USB cable is required for programming)

Wall Adapter Power Supply (Universal voltage input, US plugs)

## Device Support

You will need to following software to use this board:

**ispLEVER** for design, fitting, place & route of Lattice programmable devices

**ispVM** to download your program to the LatticeXP2 device

## Ordering Information

LFXP2-17E-L-EVN
To purchase, visit our **online store**.